

WHAT IS CLAIMED IS:

1. An image display element comprising:
 - a plurality of data lines that supply display signals;
 - a plurality of scan lines that supply scan signals;
 - 5 a first pixel electrode and a second pixel electrode that are supplied with display signals from one data line;
 - a first electrostatic shielding unit that shields the first pixel electrode from an electric field produced by a data line that is adjacent to the first pixel electrode; and
 - 10 a second electrostatic shielding unit that shields the second pixel electrode from an electric field produced by a data line that is adjacent to the second pixel electrode.
2. The image display element according to claim 1, further comprising:
 - 15 a first switching device that controls a supply of the display signal in the one data line, wherein the first switching device is electrically connected between the one data line and the first pixel electrode and has a gate electrode;
 - 20 a second switching device that is electrically connected between the gate electrode of the first switching device and a predetermined scan line; and
 - a third switching device that is connected to the one data line and that controls a supply of the display signal to the second pixel
 - 25 electrode.

3. The image display element according to claim 1, wherein
the first electrostatic shielding unit is formed by a first
conductive layer that is disposed adjacent to the data line in a lower
layer than the first pixel electrode, and
- 5 the second electrostatic shielding unit is formed by a second
conductive layer that is disposed adjacent to the data line in the lower
layer than the second pixel electrode.
4. The image display element according to claim 1, wherein
10 the first electrostatic shielding unit and the first pixel electrode
have areas that are partially superimposed with each other in a
direction that is perpendicular to the surface of layers, and
the second electrostatic shielding unit and the second pixel
electrode have areas that are partially superimposed with each other in
15 the direction that is perpendicular to the surface of layers.
5. The image display element according to claim 4, further
comprising:
a first capacitor line that is disposed in an area partially
20 superimposed with the first pixel electrode in the direction that is
perpendicular to the surface of layers in the peripheral lower layer of
the first pixel electrode facing the area in which the first electrostatic
shielding unit is disposed, and that is connected to the first electrostatic
shielding unit; and
25 a second capacitor line that is disposed in an area partially

superimposed with the second pixel electrode in the direction that is perpendicular to the surface of layers in the peripheral lower layer of the second pixel electrode facing the area in which the second electrostatic shielding unit is disposed, and that is connected to the
5 second electrostatic shielding unit.

6. The image display element according to claim 1, wherein
the first electrostatic shielding unit and the second electrostatic shielding unit are electrically connected to each other.

10

7. The image display element according to claim 1, wherein
the first electrostatic shielding unit and the second electrostatic shielding unit are electrically connected to a wiring structure that has a predetermined potential.

15

8. The image display element according to claim 1, wherein
the first electrostatic shielding unit and the second electrostatic shielding unit are connected to a predetermined scan line.

20 9. The image display element according to claim 1, wherein
the first electrostatic shielding unit and the second electrostatic shielding unit are connected to a potential supply line that has a predetermined potential.

25

10. The image display element according to claim 9, wherein the predetermined potential is maintained within a range of a potential variation of the pixel electrode.

5 11. The image display element according to claim 9, wherein the predetermined potential is maintained within a range of a potential variation of a common electrode that is disposed on a counter substrate disposed opposite to a substrate on which the pixel electrode is disposed with a predetermined distance between the substrates.

10

12. An image display device, comprising:

a data line driving circuit that supplies a display signal to a plurality of data lines;

15 a scan line driving circuit that supplies a scan signal to a plurality of scan lines;

a first pixel electrode and a second pixel electrode that are supplied with display signals from one data line;

20 a first electrostatic shielding unit that shields the first pixel electrode from an electric field produced by a data line that is adjacent to the first pixel electrode; and

a second electrostatic shielding unit that shields the second pixel electrode from an electric field produced by a data line that is adjacent to the second pixel electrode.

25

13. The image display device according to claim 12, further comprising:

a first switching device that controls a supply of the display signal in the one data line, wherein the first switching device is

5 electrically connected with the one data line and the first pixel electrode and has a gate electrode;

a second switching device that is disposed between the gate electrode of the first switching device and a predetermined scan line; and

10 a third switching device that is connected to the one data line and that controls a supply of the display signal to the second pixel electrode.

14. The image display device according to claim 12, wherein

15 the first electrostatic shielding unit and the second electrostatic shielding unit are connected to a predetermined scan line.

15. The image display device according to claim 12, wherein

the first electrostatic shielding unit and the second electrostatic
20 shielding unit are connected to a potential supply line that has a predetermined potential.

16. The image display device according to claim 15, wherein the predetermined potential is maintained within a range of a potential

25 variation of the pixel electrode.